

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:	
Prasenjit Biswas et al.	Examiner: Unknown
Serial No.: Unknown	Art Unit: Unknown
Filed: March 8, 2004	
For: FLOATING POINT UNIT PIPELINE SYNCHRONIZED WITH PROCESSOR PIPELINE	

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR §§1.97-1.98**

Sir:

In accordance with the duty of disclosure under 37 CFR §1.56 and pursuant to 37 CFR §§1.97-1.98, Applicants hereby notify the U.S. Patent and Trademark Office of the references listed on the attached Form PTO-1449. Pursuant to the PTO's waiver of the requirements under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent/patent application for new applications filed after June 30, 2003, we are not submitting copies of the U.S. patents cited. However, we are enclosing copies of all non-patent literature documents cited.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicants reserve the right to dispute any of the listed documents as prior art during examination. Furthermore, Applicants do not waive any right to take any action that

would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application. The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other material information may exist.

The Examiner is requested to initial the enclosed Form PTO-1449 and return a copy thereof to the undersigned.

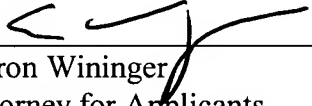
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Dated: 3/8/04

Respectfully Submitted,
Prasenjit Biswas et al.

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Complete if Known

<i>Application Number</i>	Unknown
<i>Filing Date</i>	March 8, 2004
<i>First Named Inventor</i>	Prasenjit Biswas
<i>Art Unit</i>	Unknown
<i>Examiner Name</i>	Unknown
<i>Attorney Docket Number</i>	43496.00014

Sheet	1	of	2	Attorney Docket Number	43496.00014
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[illegible][illegible]Date
Considered

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Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 2 of 2

Complete if Known

Application Number	Unknown
Filing Date	March 8, 2004
First Named Inventor	Prasenjit Biswas
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	43496.00014

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		SMITH, James E. et al.; "Implementing Precise Interrupts in Pipelined Processors"; IEEE Transactions on Computers; May 1988; pages 562-573; Vol. 37, No. 5.	
		HASEGAWA, Atsushi et al.; "SH3: High Code Density, Low Power"; IEEE Micro; December 1995; pages 11-19.	
		TURLEY, Jim; "Hitachi Adds FP, DSP Units to SuperHC Chips - Two New Additions Aimed at Boosting Lead in Consumer Electronics"; Microprocessor Report; December 4, 1995; pages 10-11.	
		JOHNSON, Mike; "Superscalar Microprocessor Design"; Prentice Hall; 1991; 7 pages.	
		MARGULIS, Neal; "i860 Microprocessor Architecture"; Osborne McGraw-Hill; 1990; pages 27, 37-41, 45-53, 80-94 and 205-210.	
		iAPX 86/88, 186/188 User's Manual Hardware Reference; Intel; Order No. 210912-001; 3 pages, including page 3-8 (8087 Numeric Processor Extension).	

Examiner
SignatureDate
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

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